

# We are looking for brains

*We are a fabless semiconductor company looking for an ASIC verification manager to work in our headquarters at the Lake of Konstanz. You will be managing the Hyperstone chip verification and validation processes to ensure the high quality of our reliable USB, SATA, PCIe, eMMC, CF, SD and  $\mu$ SD flash memory controllers.*

## ASIC / SoC Verification Manager

### Your Tasks

- Drive and guide the verification activities incl. introducing new tools and methods
- Lead a team of verification and validation engineers
- Planning, tracking and execution of verification projects
- Advisory team member to all aspects of the ASIC development

### Your Profile

- Team player and open minded
- MSc (or equivalent) degree with minimum 7 years experience in ASIC Design & Verification
- Experience in a technical leader or manager role
- Excellence in hardware description languages (Verilog, SV) and verification methodologies
- Professional in tool handling – simulation, formal verification, linting, RTC, CTC
- Knowhow in writing and adopting C/C++ for verification and validation
- Experience in FPGA prototyping and embedded HW/SW design is a plus
- Experience with complex host interfaces like PCIe, eMMC, SD, etc. is a plus
- Excellent written and verbal communication skills in English and German

### Your Benefits

- Working in a flexible, dynamic, mid-size company in a growing, high-tech market segment
- Well-equipped working infrastructure with state-of-the-art EDA tools
- Short decision making processes within a motivated team
- Working directly at the Lake of Constance
- Flexible working hours and an adequate compensation package

**If you are interested, please send your application to [jobs@hyperstone.com](mailto:jobs@hyperstone.com)**

**[www.hyperstone.com](http://www.hyperstone.com)**