hyperstone®

Develop with us

Creative thinkers welcome

ASIC Verification Specialist

For our hardware design team, located in Konstanz, Germany

Your Tasks

- Development of verification solutions for systems or sub-systems as part of our ASIC development activity
- Participation in the decision making process about third party IP/VIP versus in-house development
- Functional verification planning incl. feature extraction and test case / regression development
- Maintain and improve the verification- and script environment
- Conceptual and architectural considerations and decisions together with systems engineers
- Develop and maintain prototyping solutions combined with testware development
- Support the introduction of new methods and tools

Your Profile

- Degree (MSc) in Electrical Engineering, Digital Design, Electronics or equivalent,
- 5 years+ experience in digital design and verification
- Experience with hardware description languages like Verilog, SystemVerilog, etc
- Experience with modern verification methodologies e.g. OVM, UVM
- Knowledge in simulation environment, equivalence checking, linting and formal verification
- Good knowledge in writing and adopting C/C++ code for verification
- Excellent written and verbal communication skills in English.
- Prototyping/FPGA experience
- Knowledge in Flash Memory Design and ECC technology is a plus

Your Benefits

- Working in a flexible, dynamic, mid-size company in a growing, high-tech market segment
- Well-equipped working infrastructure with state-of-the-art EDA tools
- Short decision making processes within a motivated team
- Further training and course opportunities
- Flexible working hours and an adequate compensation package
- Working directly at the Lake of Constance

If you are interested, please send your application to jobs@hyperstone.com

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