



Hard/Software Validation Engineer (m/f/d)

Your Tasks

Hyperstone develops NAND flash memory controllers for industrial applications. Our engineering teams in Konstanz develop state-of-the-art chips/ASICs for this purpose as well as firmware solutions especially tailored to our customers.

Our products must be thoroughly tested in our laboratories in order to maintain our quality in the long term. This requires the development of dedicated PCBs, test series must be carried out, chips must be characterized, and much more. In your function you will lead the work in our laboratory and help to continuously optimize the processes.

Your Profile

- At least 5 years of experience in the validation of electronic systems
- Experience in PCB development and assembly
- Experience in operating and maintaining test computer systems
- Experience in handling FPGA systems and ideally also HDL experience
- Familiar with interfaces like I2C, JTAG, UART, SPI, SD Card or eMMC
- Experience and safe handling of test equipment such as oscilloscope, logic analyser, protocol analyser for troubleshooting on board and system level
- Experience with Linux/Unix and Windows systems
- Familiar with the mechanical design of boards and systems
- Experience with Signal Integrity (SI) and Power Distribution Network (PDN) simulations is an advantage
- Experience with >10Gb/s SERDES board designs is desirable

Your Benefits

- ✓ Short decision making processes and open communication culture
- ✓ Corporate Benefits
- ✓ Working in a growing storage segment
- ✓ Well equipped working infrastructure with SotA EDA
- ✓ Living directly at the beautiful Lake Constance
- ✓ Flexible working hours and an adequate compensation package
- ✓ Motivated team of employees

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