

# How's your Memory?

*with us you can develop the best...*

## ASIC Design & Verification Engineer

*For our hardware design team, located in Konstanz, Germany*

### Your Tasks

- Support the design and verification activity as part of our ASIC development team
- Enrich our development teams with fresh ideas and smart concepts
- Step by step learn about the different tasks, needed to develop successful Chips
- Training combined with learning by doing and stepwise take over tasks and responsibilities
- Participate to conceptual and architectural considerations together with our systems engineers
- Develop and maintain prototyping solutions combined with testware development
- Support the introduction of new methods and tools

### Your Profile

- Degree (MSc) in Electrical Engineering, Digital Design, Electronics or equivalent,
- Interest in digital Chip design and verification
- First experience with hardware description languages like Verilog, VHDL, SystemVerilog
- Good programming knowledge (C/C++/SystemC)
- Good written and verbal communication skills in English.
- Interest in prototyping (FPGA/PLD)
- First knowledge about Flash Memory is a plus

### Your Benefits

- Working in a flexible, dynamic, mid-size company in a growing, high-tech market segment
- Well-equipped working infrastructure with state-of-the-art EDA tools
- Short decision making processes within a motivated team
- Working in an international company with offices in U.S. and Asia Pacific
- Further training and course opportunities
- Flexible working hours and an adequate compensation package
- Working directly at the Lake of Constance

**If you are interested, please send your application to [jobs@hyperstone.com](mailto:jobs@hyperstone.com)**

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