Hardware Design Engineer (m/f/d)

Your Taks

- Intensification of ASIC development activities
- Placement of new controller products
- Training period in the areas of Design-For-Test (DFT) and the
- Statistical Timing Analysis (STA)
- Medium-term assumption of project management functions

Your Profile

- Degree (MSc) in electrical engineering, communication technology, electronics or equivalent
- Sound knowledge in the field of STA and/or DFT and the necessary required EDA tools
- First experiences in the field of architectural development, Verilog/VHDL Coding, verification, Perl, Tcl shell scripting
- Good written and oral communication skills in English
- Basic knowledge of microprocessor technology and hardware-related Programming are advantages

Your Benefits

- ✔ Short decision making processes
- ✔ Corporate Benefits
- ✔ Working in a growing storage segment
- ✔ Well equipped working infrastructure with SotA EDA
- ✔ Living directing at the beautiful Lake Constance
- ✔ Flexible working hours and an adequate compensation package

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📍 Konstanz, Germany