

Performance **C**reative **I**ntelligence mbedded

For our ASIC development team, located in Konstanz, Germany, we are looking for a (m/ f/ d)

PCIe System & Validation Engineer

Your Tasks

- System Design of a PCIe flash memory controller
- Drive and guide PCIe related development activities
- Planning, tracking and execution of system development tasks
- Close cooperation inside the Hardware team and with the Firmware- and Test team
- Advisory team member to all aspects of the PCIe developments, incl. external IP etc.

Your Profile

- Open minded team player
- MSc (or equivalent) degree with minimum 5 years experience in ASIC Design & Validation
- Extensive knowledge of PCIe host interface specification and NVMe
- Excellence in hardware description languages (Verilog, SV)
- Professional in tool handling - simulation, formal verification, linting, RTC, CTC
- Knowhow in writing and adopting C/C++ for verification and behavioral modelling
- Excellent written and verbal communication skills in English and German
- Experience in FPGA prototyping and embedded HW/SW design is a plus
- Experience with other host interfaces such as eMMC, SD, etc. is a plus

Your Benefits

- Working in a flexible, dynamic, mid-size company in a growing, high-tech market segment
- Well-equipped working infrastructure with state-of-the-art EDA tools
- Short decision making processes within a motivated team
- Working and Living at the Lake of Constance and close to the Alps and Switzerland
- Flexible working hours and an adequate compensation package

If you are interested, please send your application to jobs@hyperstone.com

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